

Nanowire Crossbar Arrays as Address Decoders for Integrated Nanosystems

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Supporting Online Material

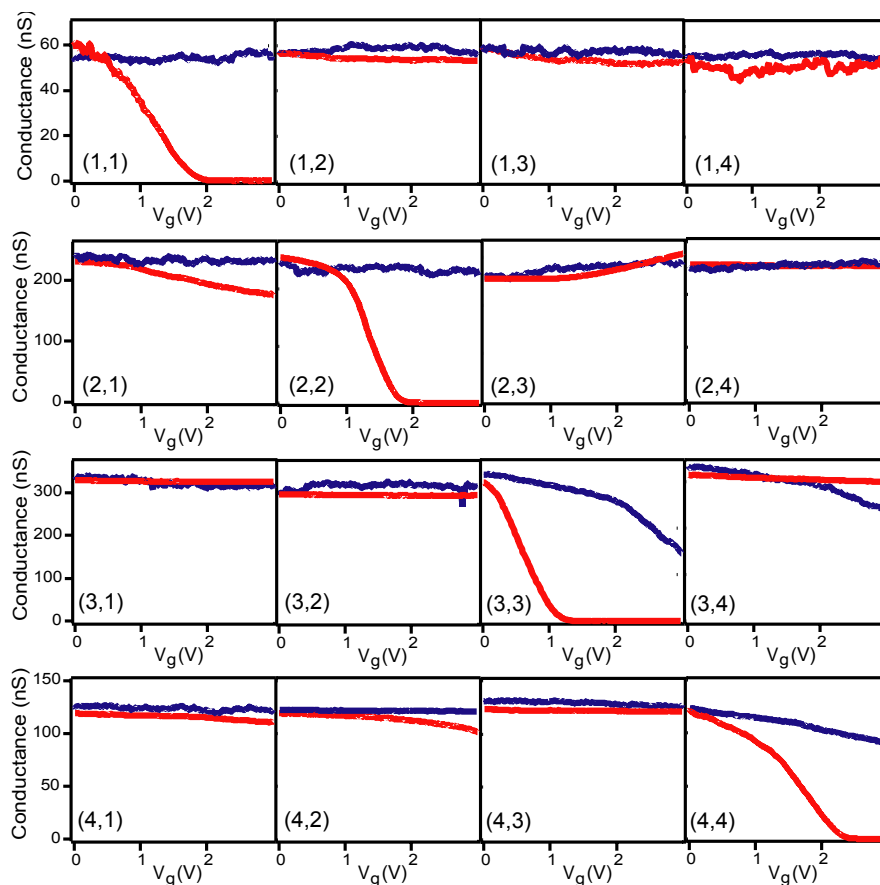


Fig. S1. Conductance vs. gate voltage for each cross point in the 4x4 cNW-FET array shown in Fig. 3D; The blue and red curves correspond to data recorded before and after chemical modification, respectively. Each plot corresponds to one cNW junction (n, m). The four diagonal junctions, (1,1), (2,2), (3,3), and (4,4), were modified in this array.